



2819
10-15-03
XA-9472

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yasuhisa SHIMAZAKI et al.

Appln. No.: 09/855,660

Group Art Unit: 2819

Filed: May 16, 2001

Examiner: D. Chang

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING HIGH-SPEED
AND LOW-POWER LOGIC GATES WITH COMMON TRANSISTOR
SUBSTRATE POTENTIALS, AND DESIGN DATA RECORDING
MEDIUM THEREFOR

* * *

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any
assertion as to materiality or prior art effect, the
documents listed on the attached Form PTO-1449 are hereby
cited.

The listed Japanese documents were cited in a recent
Office Action regarding a counterpart Japanese application.
The Office Action also cited Japanese 11-195976, which is
already of record in the instant application. A partial

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English translation of the Japanese Office Action is attached.

The cited U.S. patents, No. 5,266,848 and 6,380,764 are counterparts of Japanese 04-211515 and 11-195976, respectively.

A check for \$180 is attached to cover the applicable fee under 37 C.F.R. § 1.17(p).

Respectfully submitted,

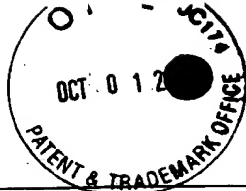
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By: 

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October 1, 2003



FORM PTO-1449				Atty. Docket No.		Appln. No.	
LIST OF DOCUMENTS CITED BY APPLICANT				XA-9472		09/855,660	
				Applicant			
				Yasuhisa SHIMAZAKI et al.			
				Filing Date		Group	
				May 16, 2001		2819	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
	AA	5,266,848	11/30/93	Nakagome et al.	307	475	
	AB	6,380,764	4/30/02	Katoh et al.	326	93	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AJ	07-050392	2/21/95	Japan			Abstract
	AK	04-211515	8/03/92	Japan			Abstract
	AL						
	AM						
	AN						
	AO						
	AP						
OTHER (including author, title, date, pertinent pages, etc.)							
	AQ						
	AR						
Examiner				Date Considered			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							